	Type	L #	Hits	Search Text	DBs	Time Stamp
1	BRS	L1		magnetic near4 (co or ni or fe) near8 (f or n or o)	US- PGPUB; USPAT; EPO; JPO; DERWEN T; IBM_TD B	2004/12/06 10:19
2	BRS	L2	795418	"integrated circuit" or IC		2004/12/06 10:19
3	BRS	L3	250924 3	substrate or wafer or chip	US- PGPUB; USPAT; EPO; JPO; DERWEN T; IBM_TD B	2004/12/06 10:20
4	BRS	L 5	51	1 and 2 and 3	US- PGPUB; USPAT; EPO; JPO; DERWEN T; IBM_TD B	2004/12/06 10:23

	Туре	L #	Hits	Search Text	DBs	Time Stamp
5	BRS	L 6	35	5 and ((@ad<"20000404") or (@rlad<"20000404"))	US- PGPUB; USPAT; EPO; JPO; DERWEN T; IBM_TD B	2004/12/06 10:44
6	BRS	L7	0	1 same 2	US- PGPUB; USPAT; EPO; JPO; DERWEN T; IBM_TD B	2004/12/06 10:41
7	BRS	L8	56	1 and 2	US- PGPUB; USPAT; EPO; JPO; DERWEN T; IBM_TD B	2004/12/06 10:41
8	BRS	L9	5	8 not 5	US- PGPUB; USPAT; EPO; JPO; DERWEN T; IBM_TD B	2004/12/06 10:41

	Туре	L #	Hits	Search Text	DBs	Time Stamp
9	BRS	L10	110997	"back surface"	US- PGPUB; USPAT; EPO; JPO; DERWEN T; IBM_TD B	2004/12/06 10:43
10	BRS	L11	1946	magnetic near8 10	US- PGPUB; USPAT; EPO; JPO; DERWEN T; IBM_TD B	2004/12/06 10:44
11	BRS	L12	44	11 same 2	US- PGPUB; USPAT; EPO; JPO; DERWEN T; IBM_TD B	2004/12/06 10:44
12	BRS	L13	23	12 and ((@ad<"20000404") or (@rlad<"20000404"))	US- PGPUB; USPAT; EPO; JPO; DERWEN T; IBM_TD B	2004/12/06 10:44